

FIG.1

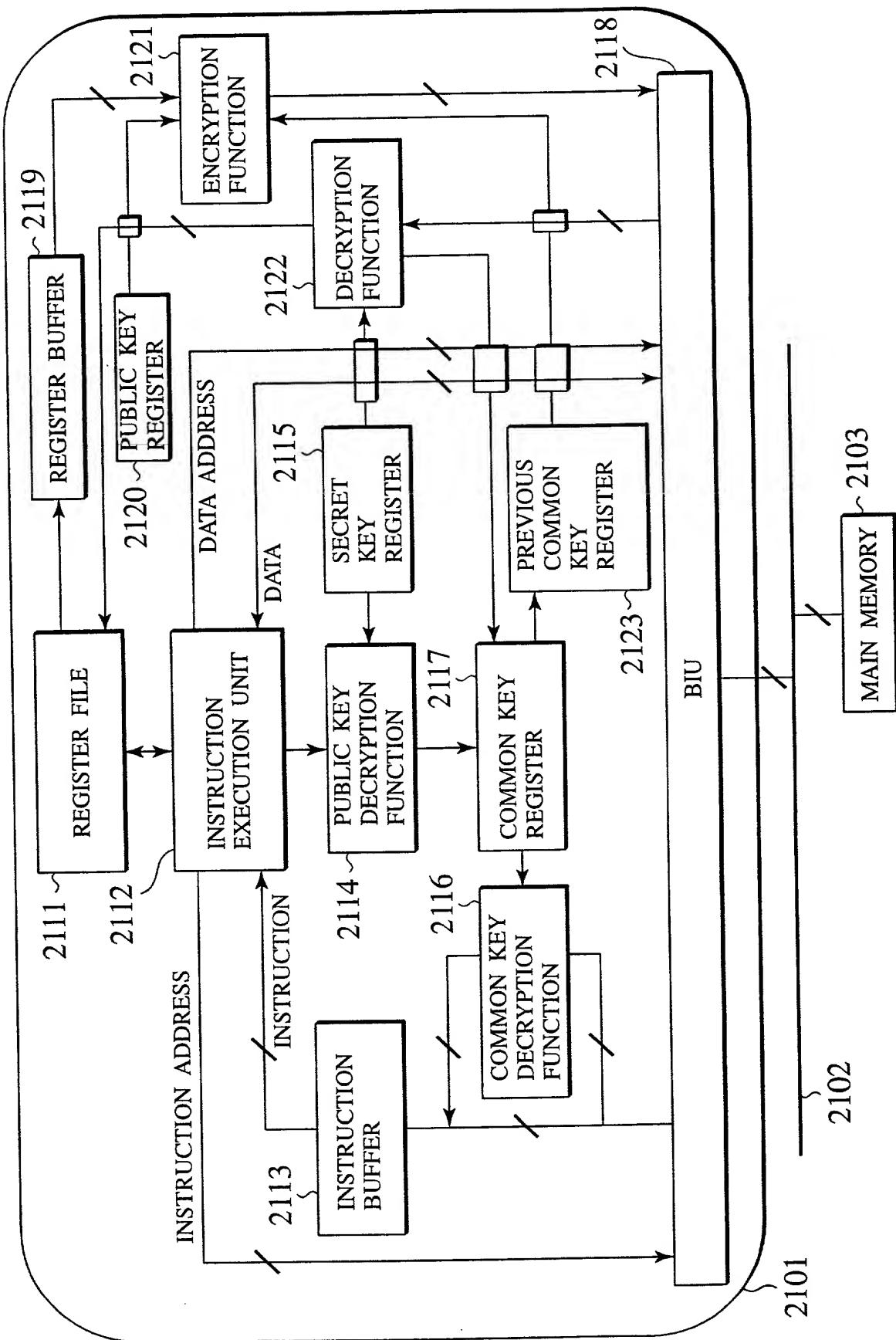


FIG.2

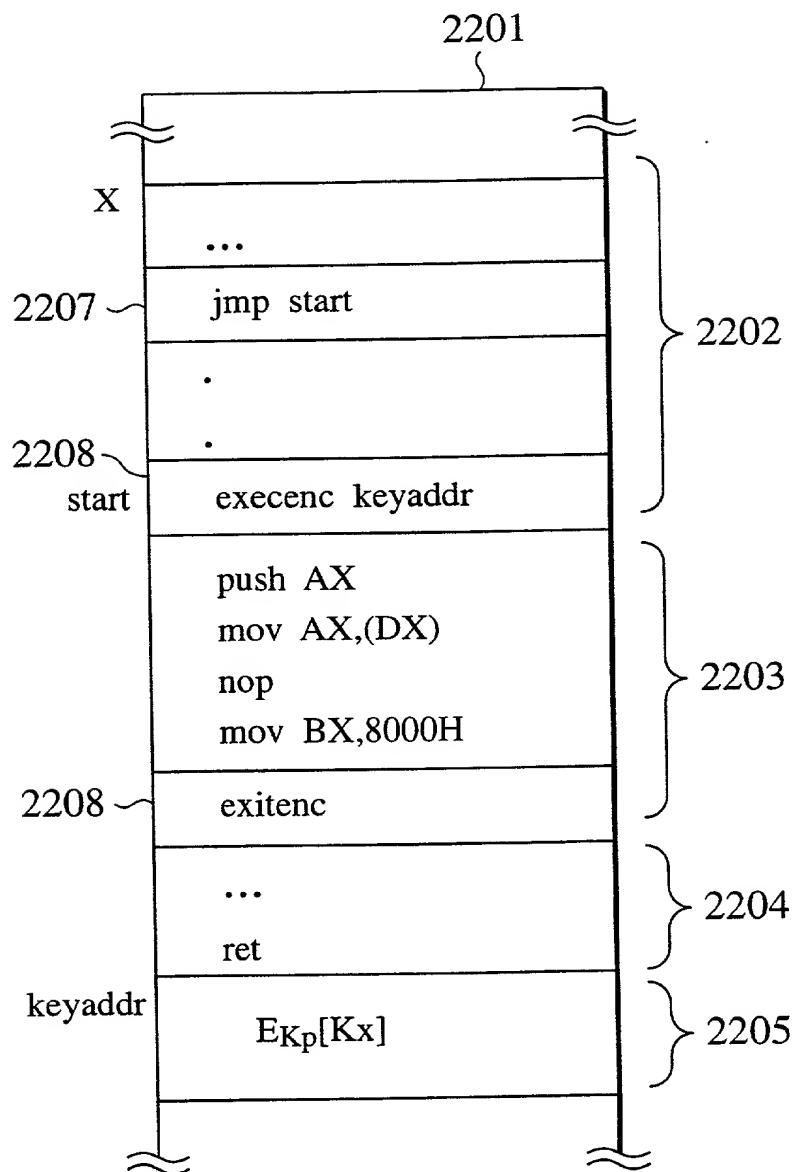
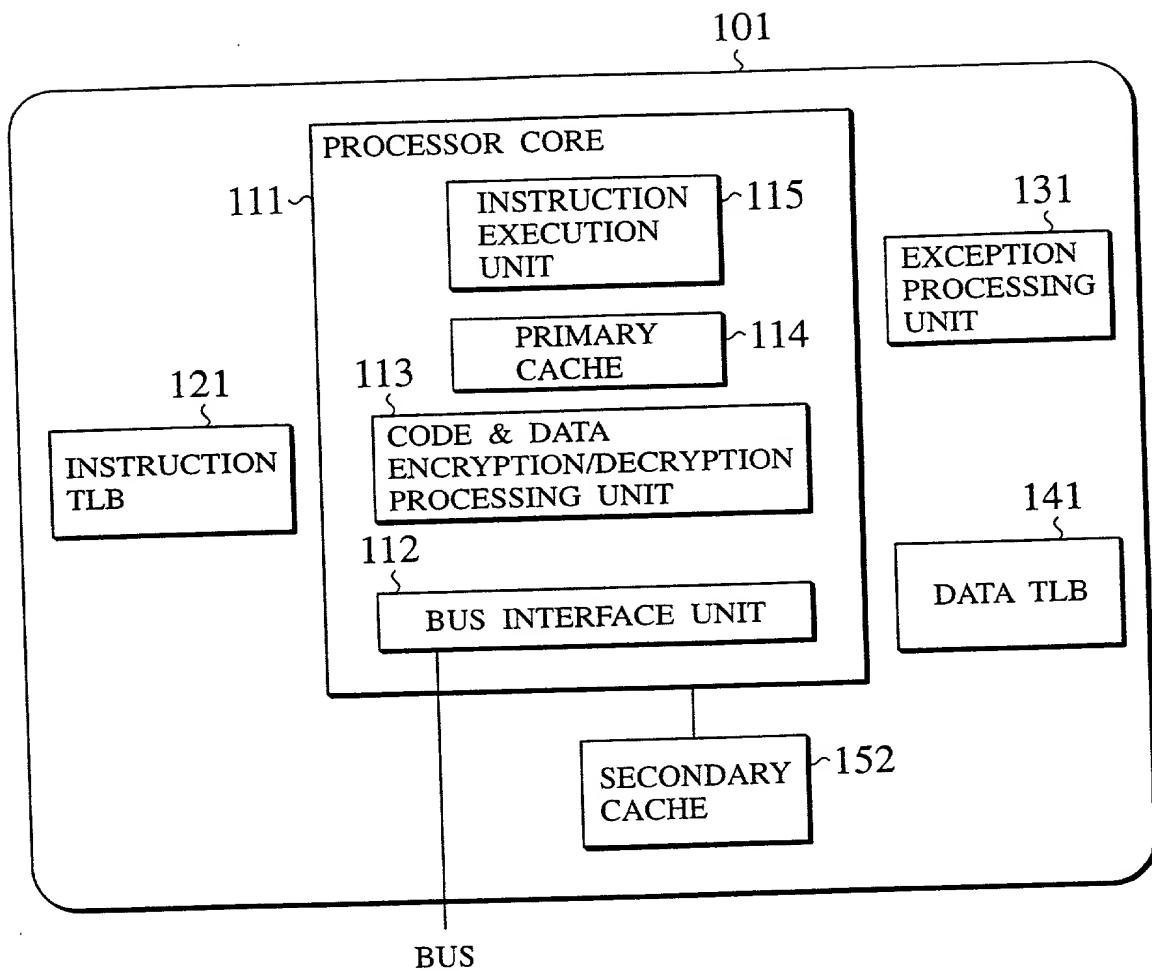


FIG.3



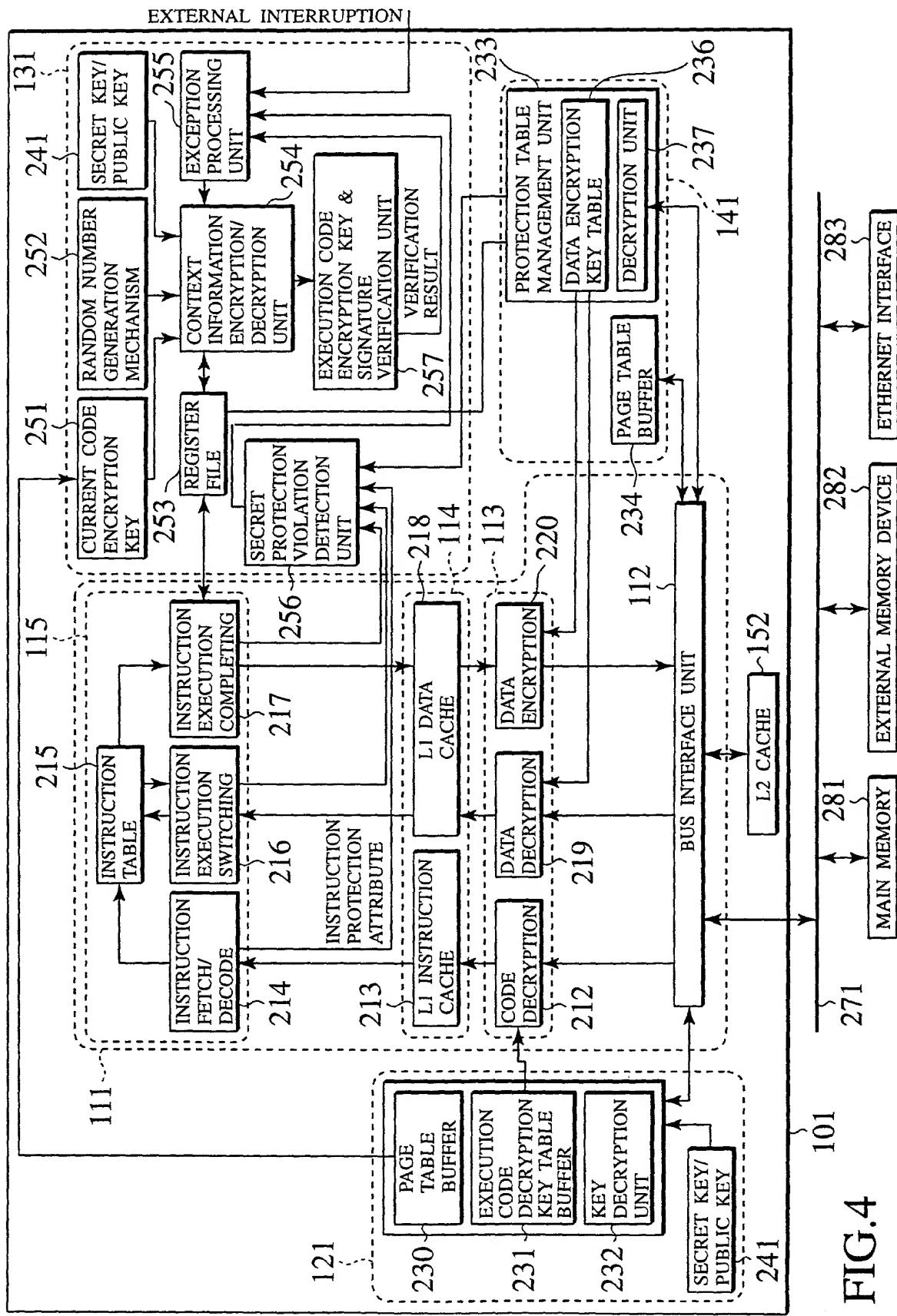
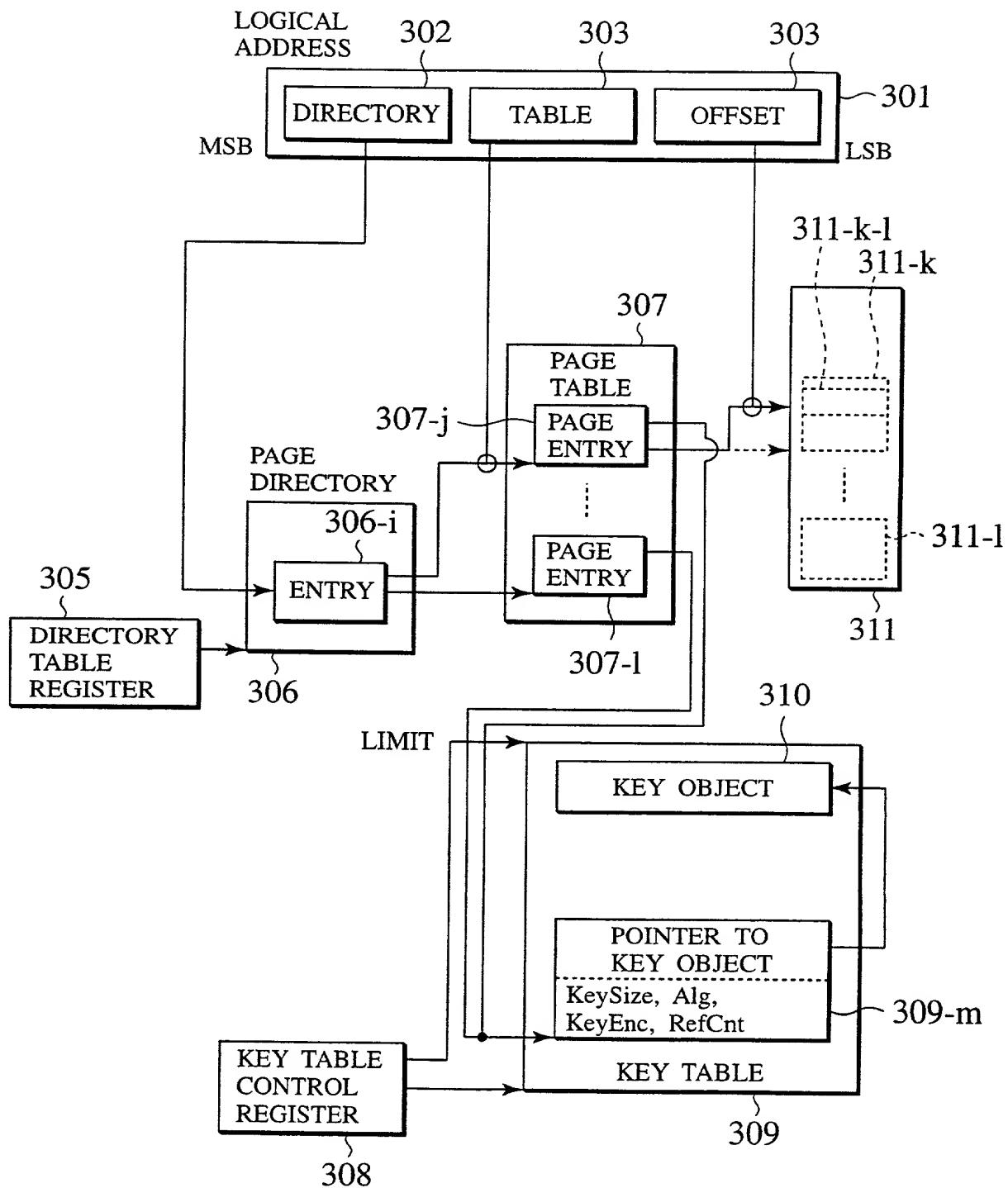


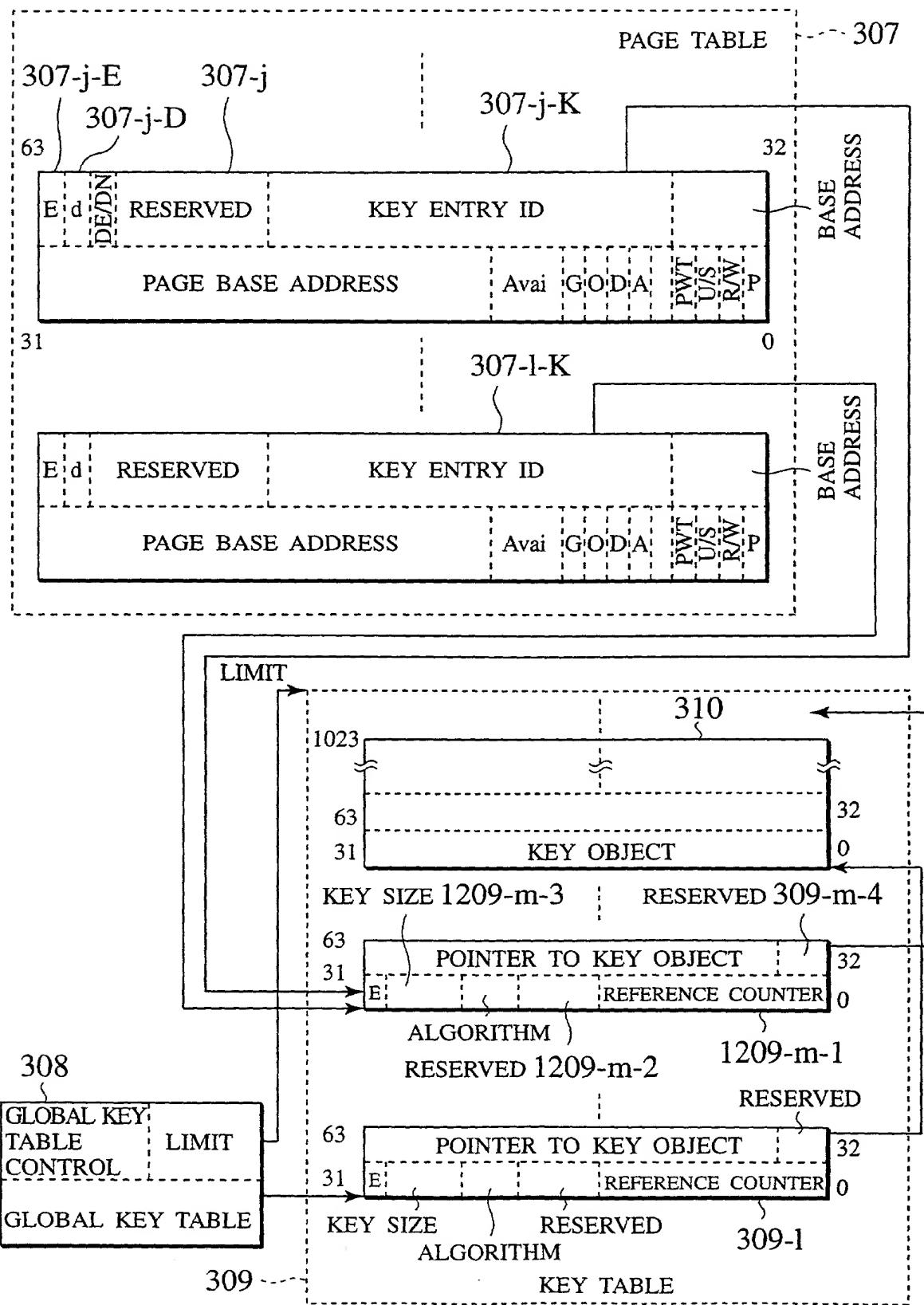
FIG.4

FIG.5



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FIG.6



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FIG.7A

	0	1	2	3
0	A0	A1	A2	A3
1	B0	B1	B2	B3
2	C0	C1	C2	C3
3	D0	D1	D2	D3
4	E0	E1	E2	E3
5	F0	F1	F2	F3
6	G0	G1	G2	G3
7	H0	H1	H2	H3

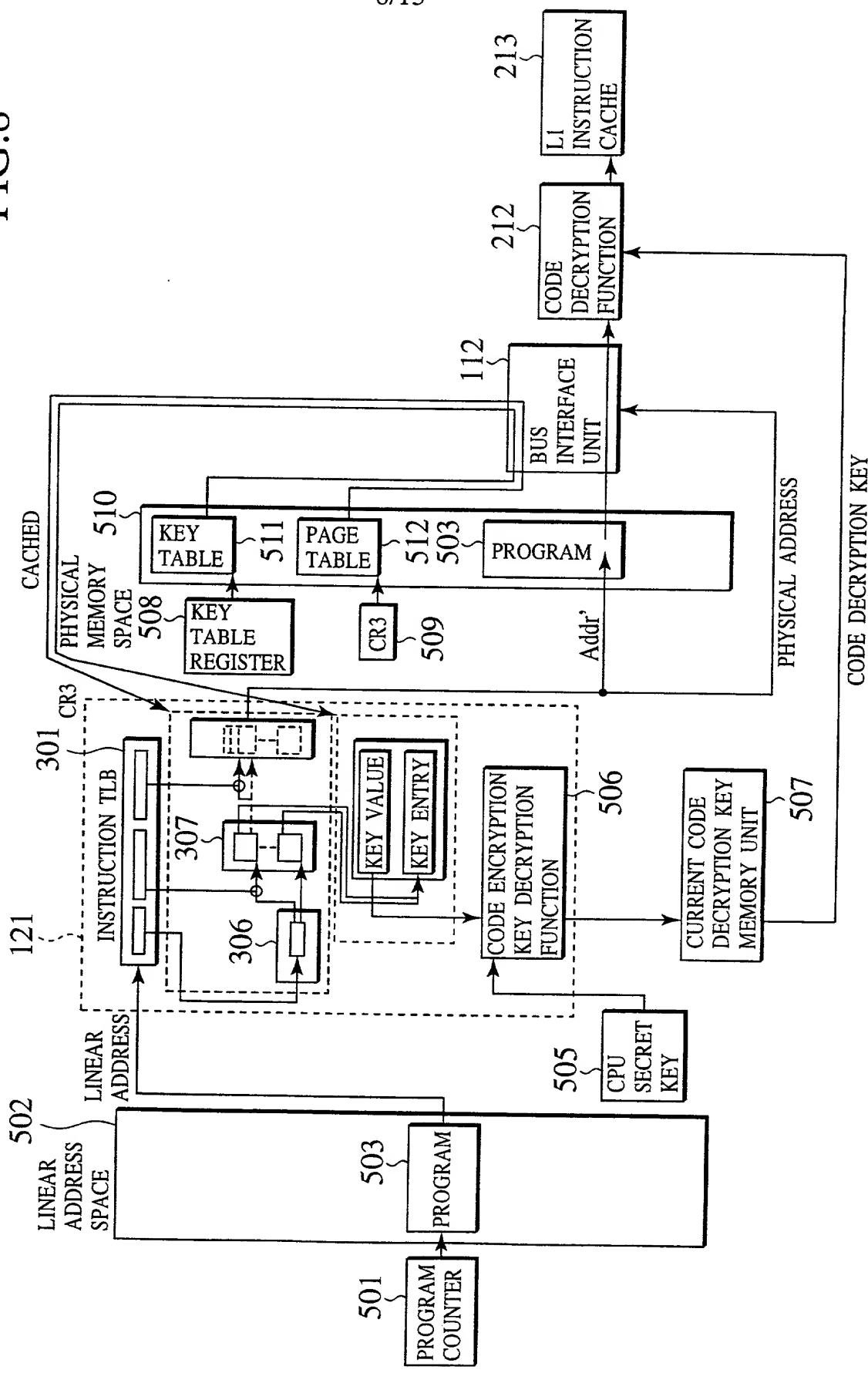
BEFORE INTERLEAVING

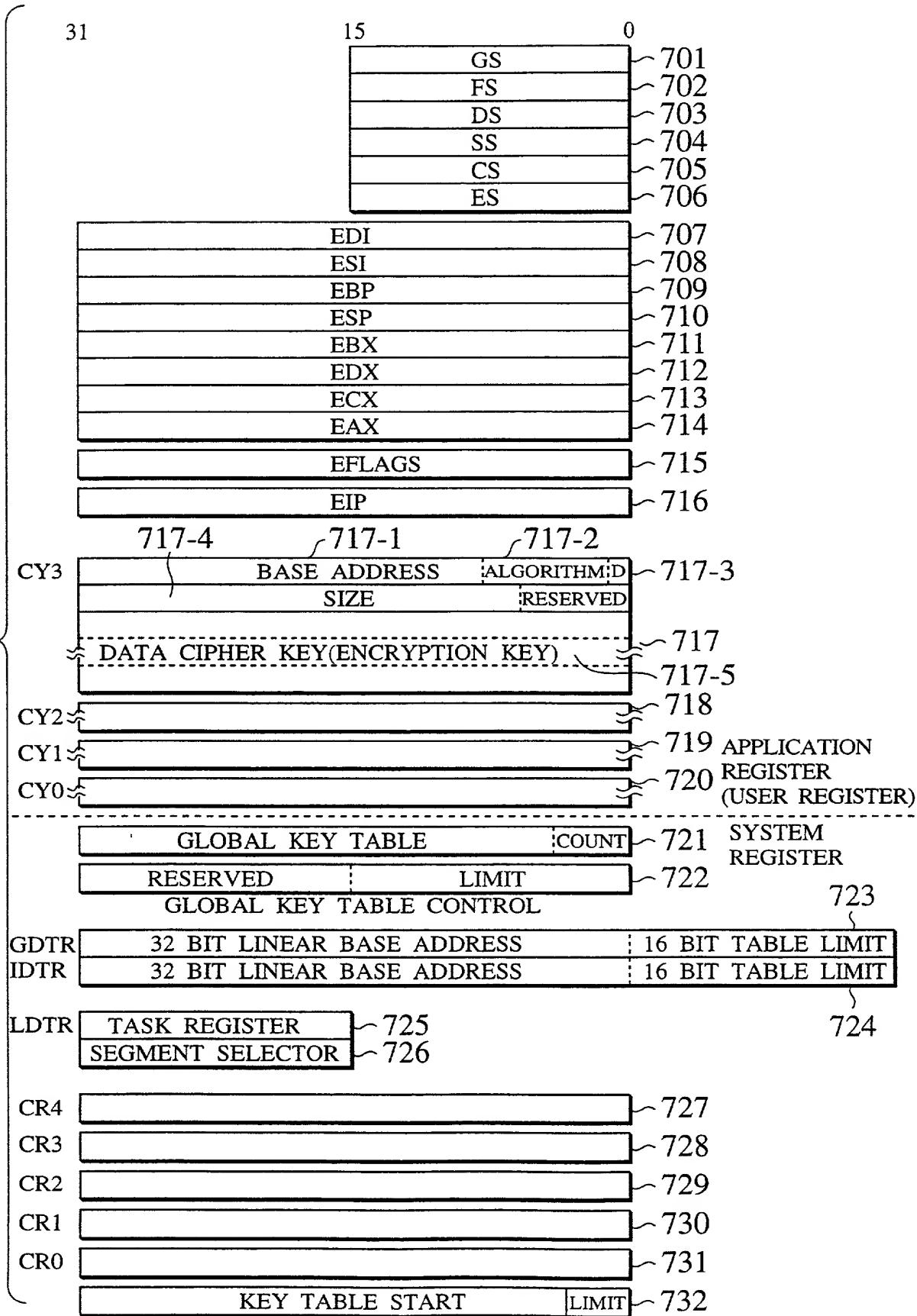
FIG.7B

	A0	B0	C0	D0
0	A0	B0	C0	D0
1	E0	F0	G0	H0
2	A1	B1	C1	D1
3	E1	F1	G1	H1
4	A2	B2	C2	D2
5	E2	F2	G2	H2
6	A3	B3	C3	D3
7	E3	F3	G3	H3

AFTER INTERLEAVING

FIG. 8



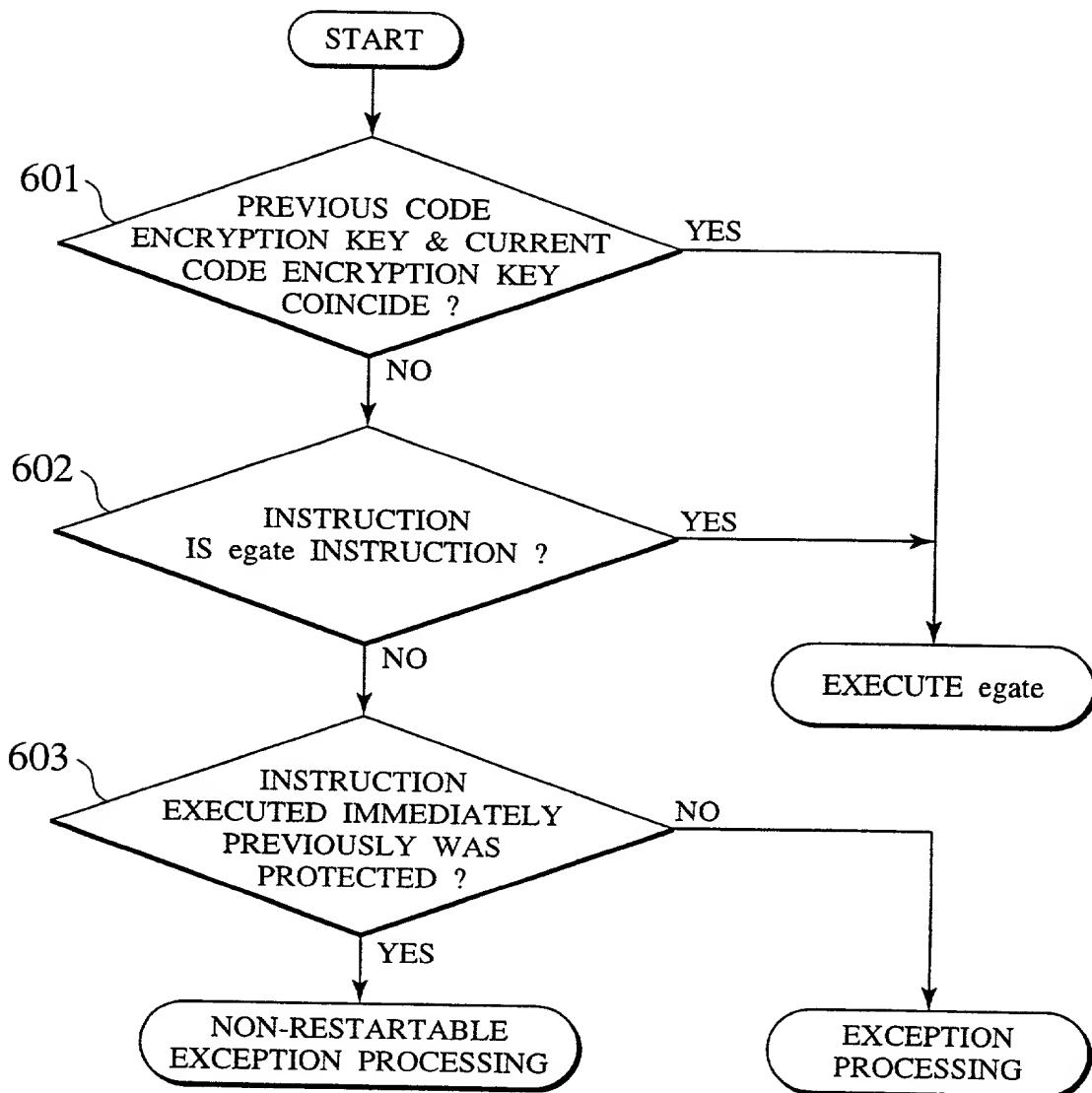


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## FIG.10

S[message] by Ks	834
E[Kr] by Kp	833
E[Kr] by Kcode	832
PADDING	831
BASE ADDRESS	RESERVED:E
SIZE	
DATA ENCRYPTION KEY FOR CY3	830
DATA ENCRYPTION CONTROL REGISTER (CY2)	829
DATA ENCRYPTION CONTROL REGISTER (CY1)	828
DATA ENCRYPTION CONTROL REGISTER (CY0)	827
TSS SIZE	E
I/O MAP BASE ADDRESS	U R T
LDT SEGMENT SELECTOR	825-1
GS	825-2
FS	
DS	
SS	
CS	
ES	
EDI	817
ESI	816
EBP	815
ESP	814
EBX	813
EDX	812
ECX	811
EAX	810
EFLAGS	809
EIP	808
CR3(PDBR)	807
SS2	806
ESP2	805
SS1	804
ESP1	803
SS0	802
ESPO	801
LINK TO PREVIOUS TASK	

FIG.11



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FIG.12

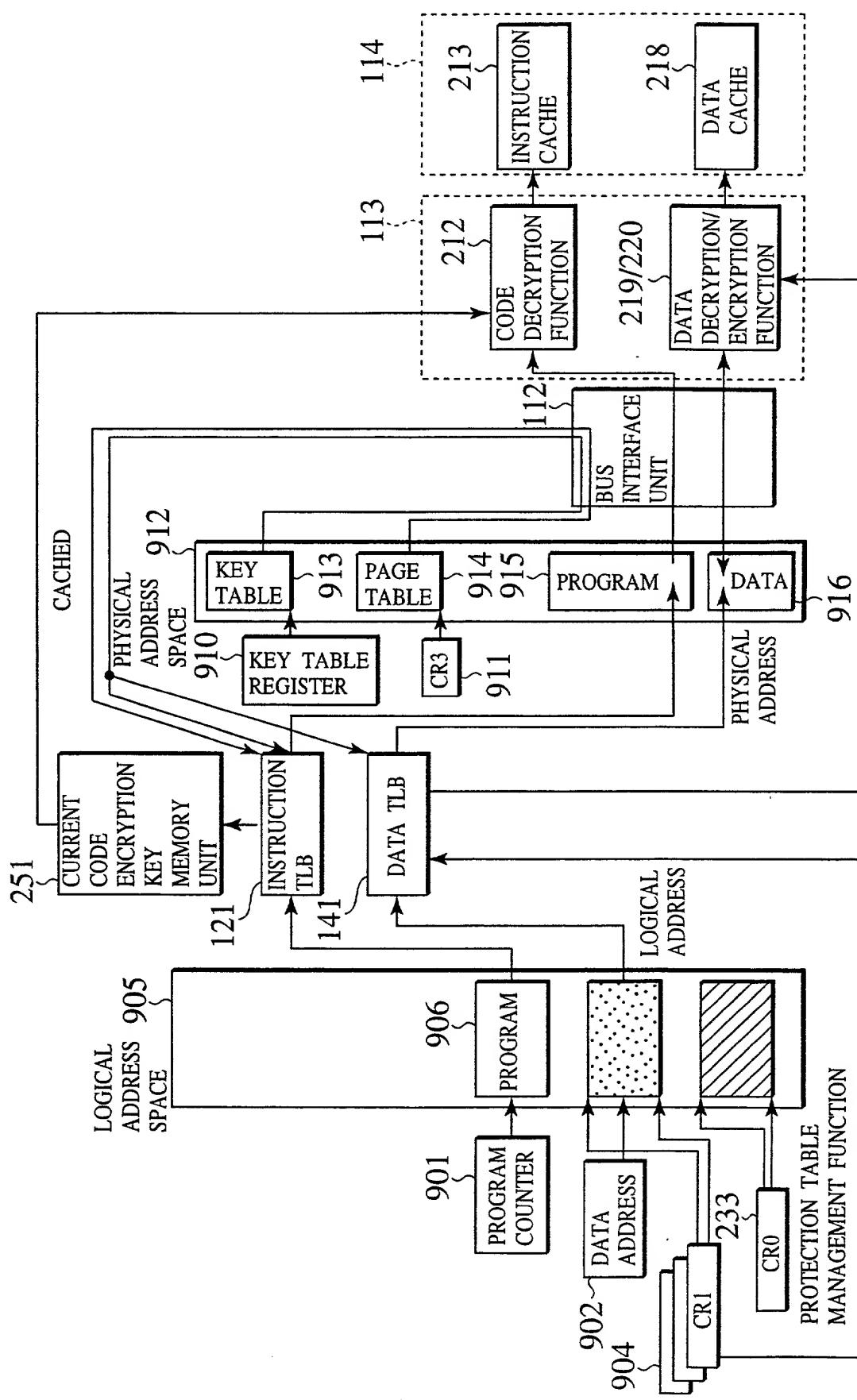


FIG.13

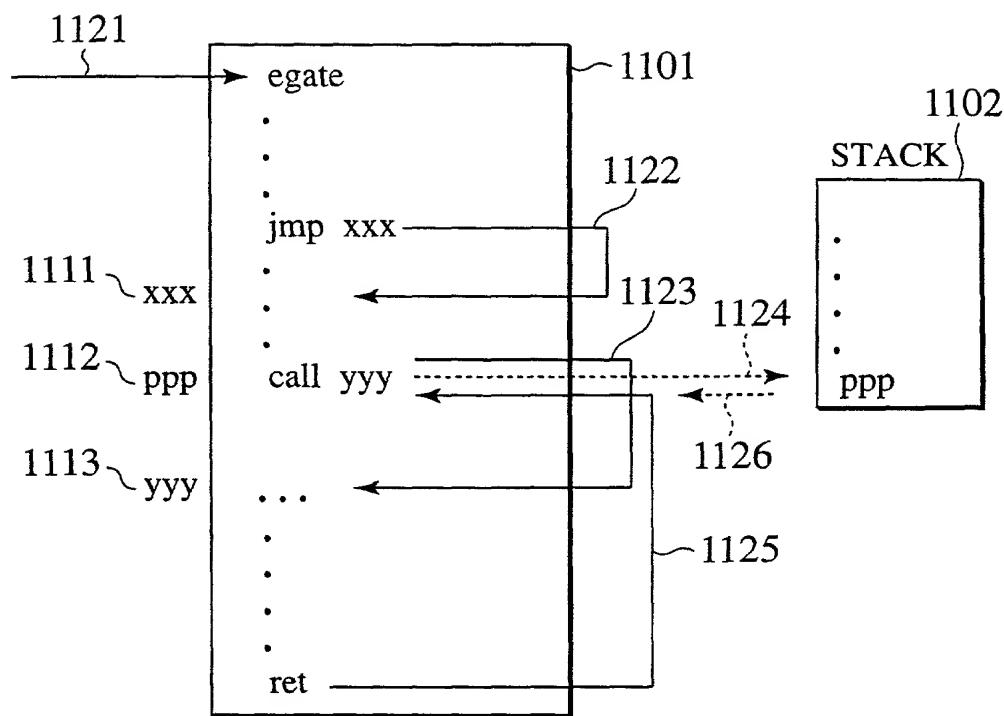
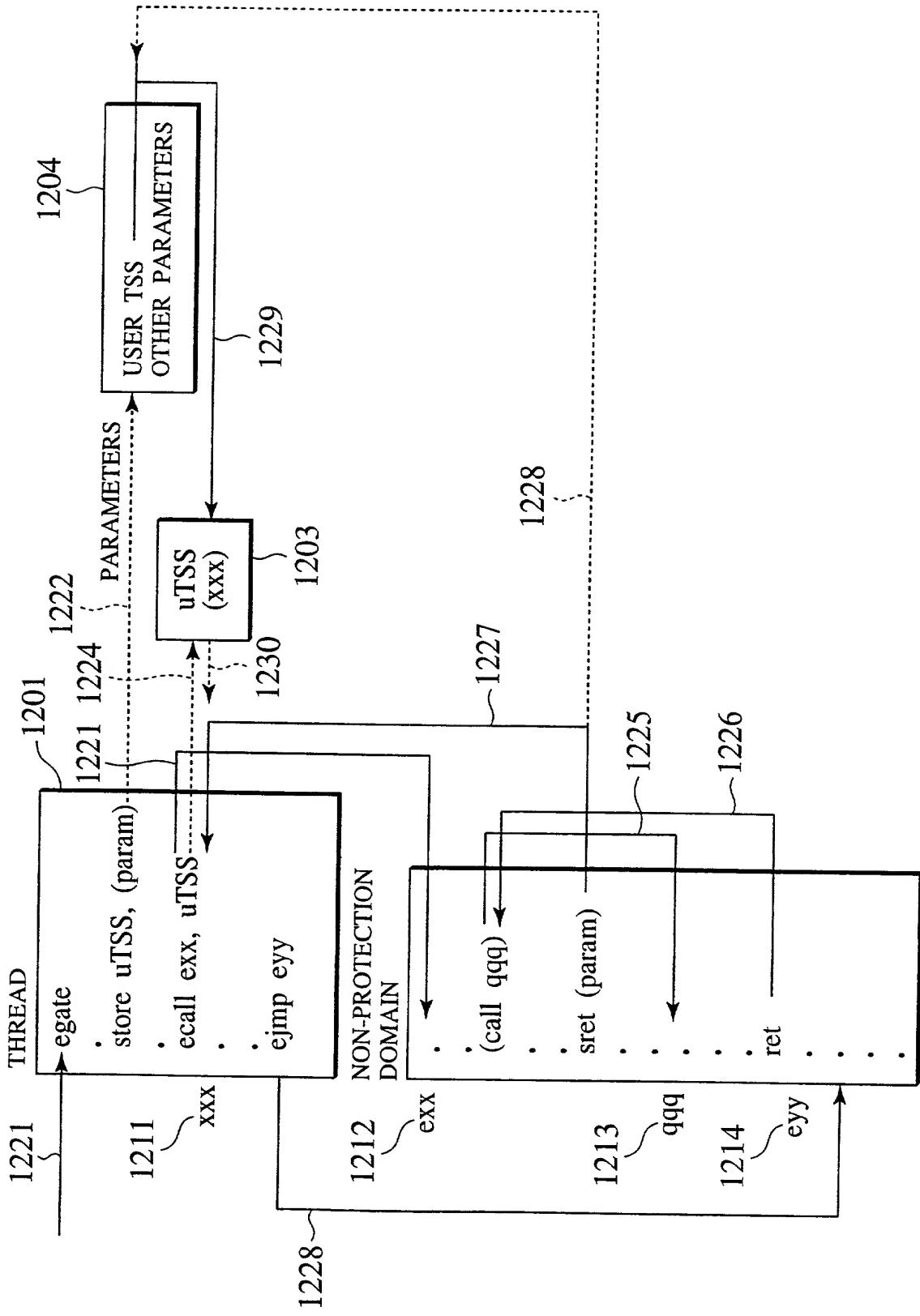


FIG.14



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**FIG.15**  
**PRIOR ART**

31	15	0
		T 100
		96
		92
		88
		84
		80
		76
		72
		68
		64
		60
		56
		52
		48
		44
		40
		36
		32
		28
		24
		20
		16
		12
		8
		4
		0
		LINK TO PREVIOUS TASK